WARP Analog Board

Revision 1.1

Schematic Pages

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Title
ADC/DAC Daughtercard

Description
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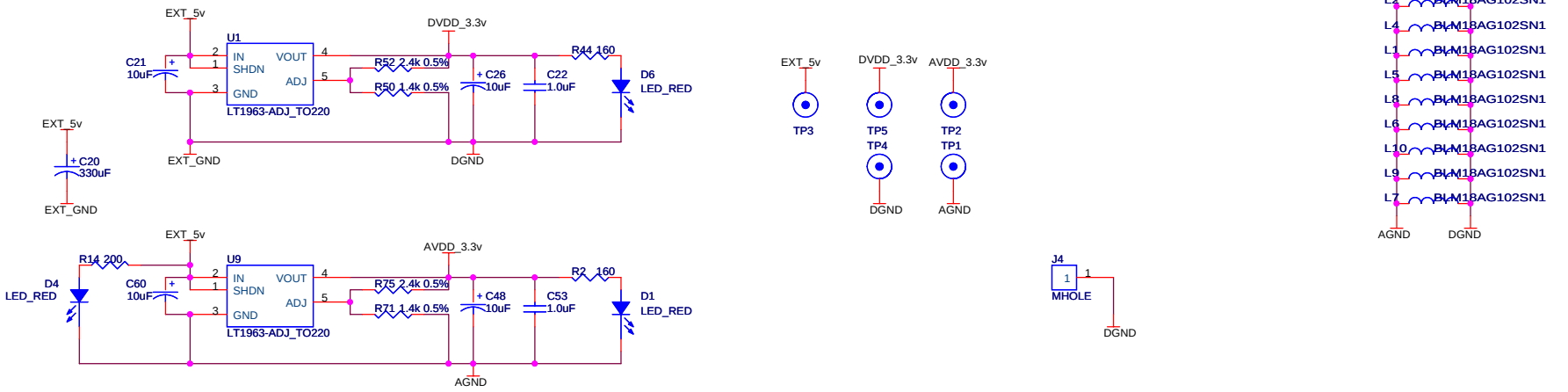
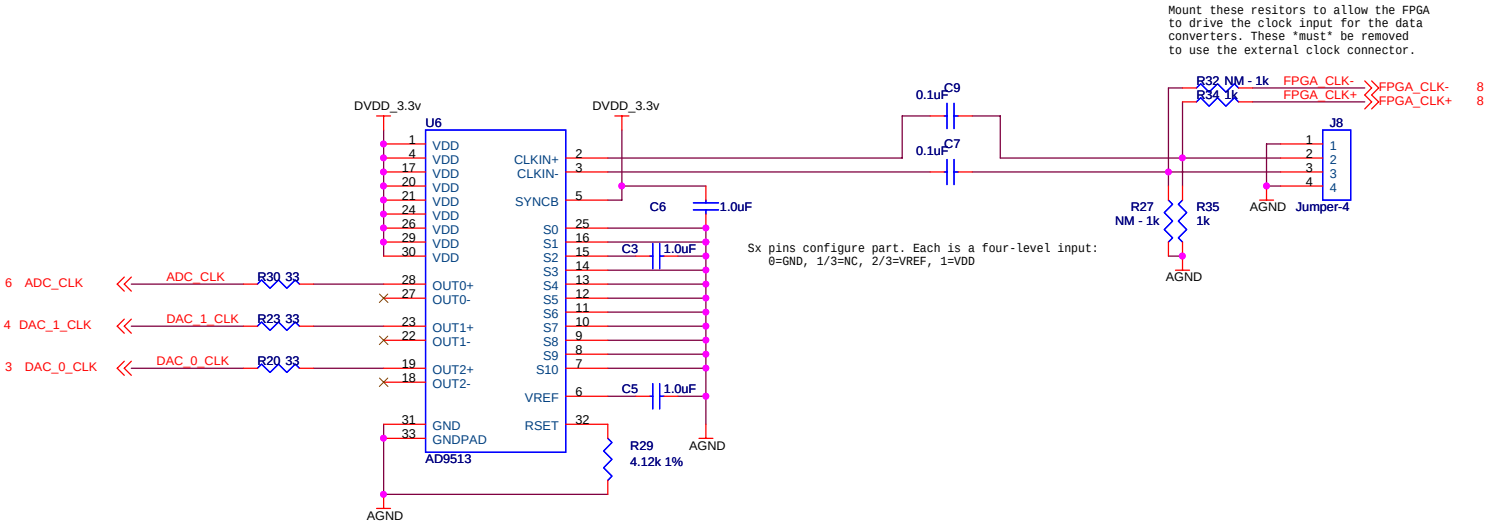
Analog/Digital Ground Filtering





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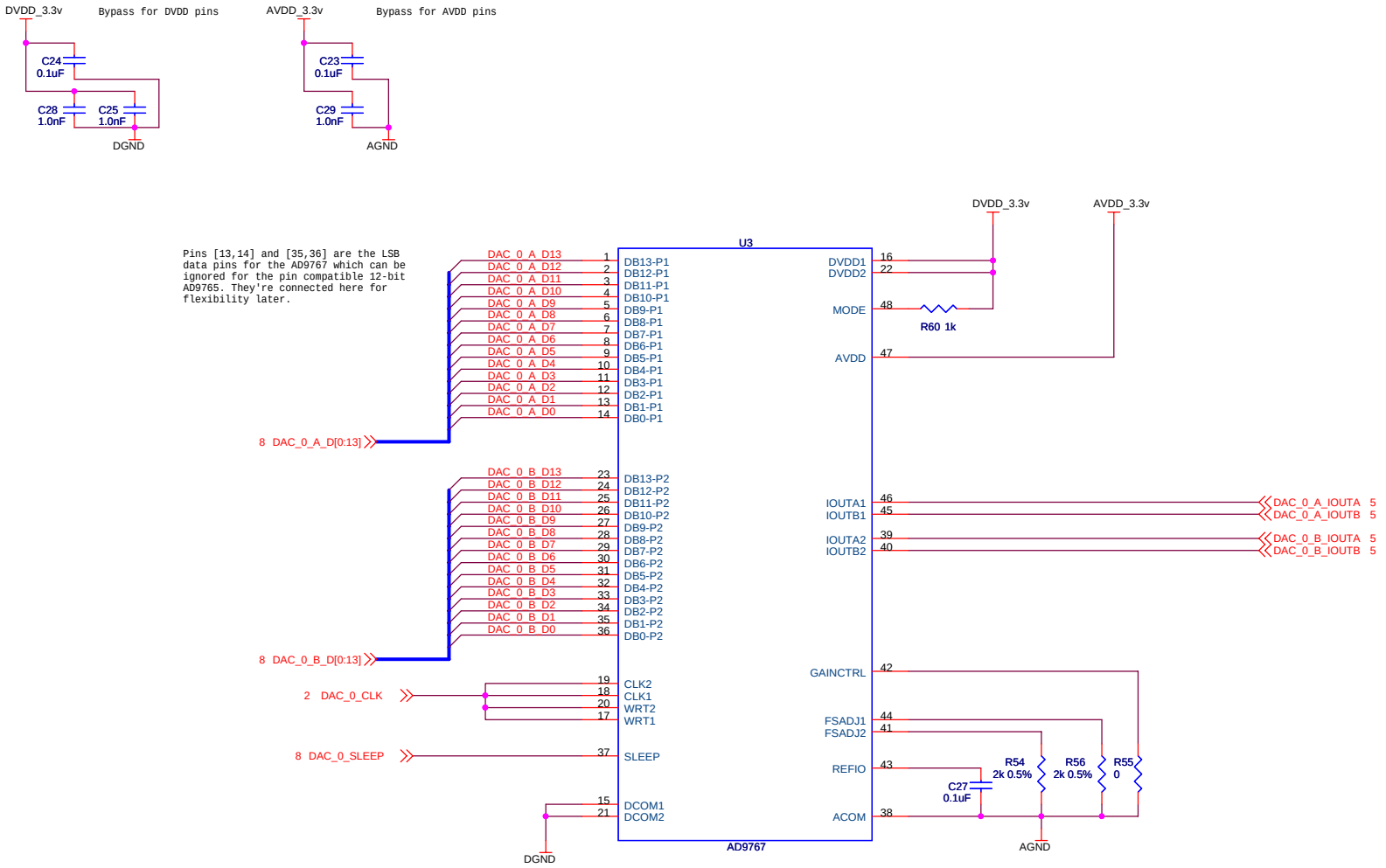

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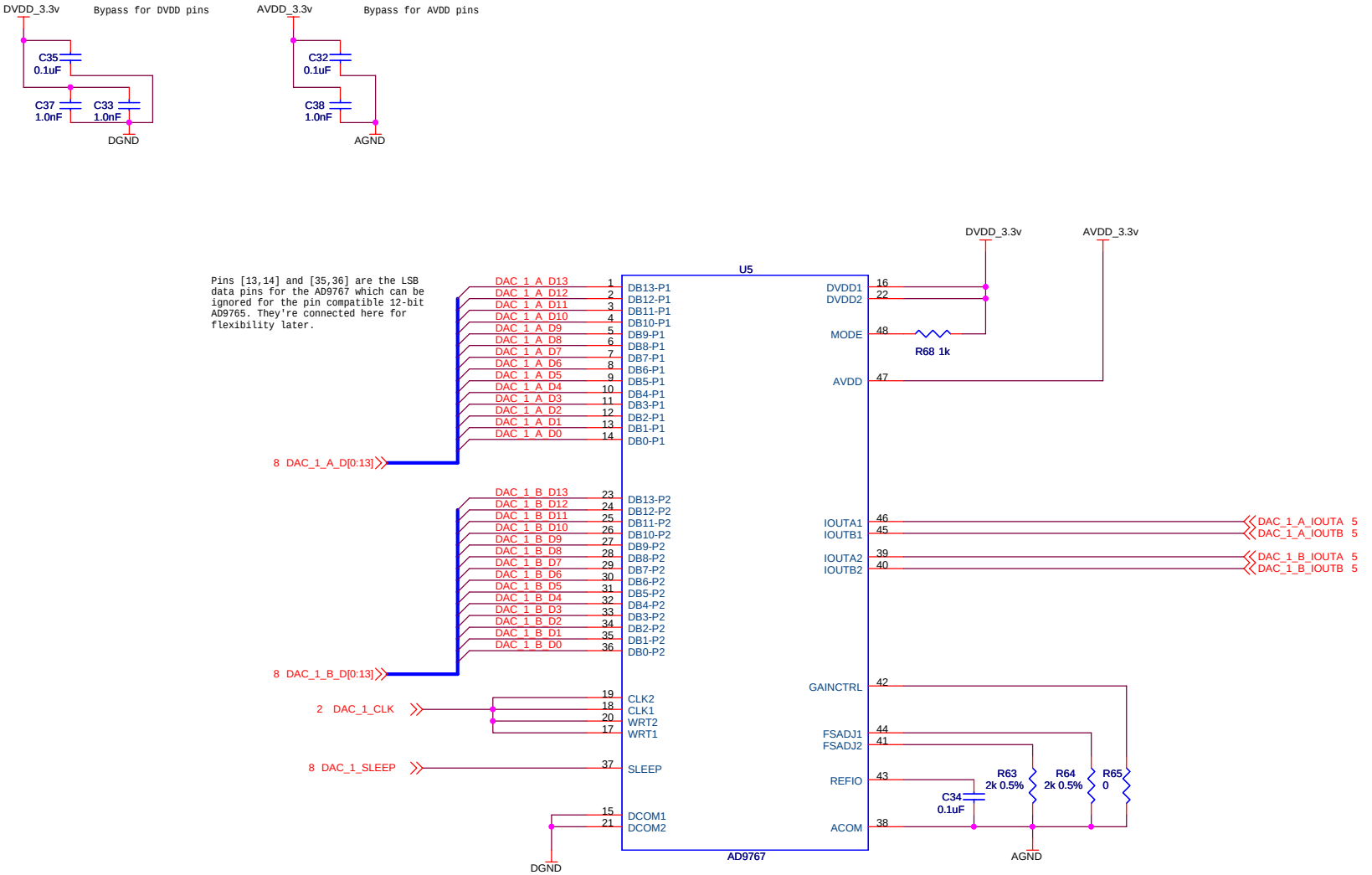

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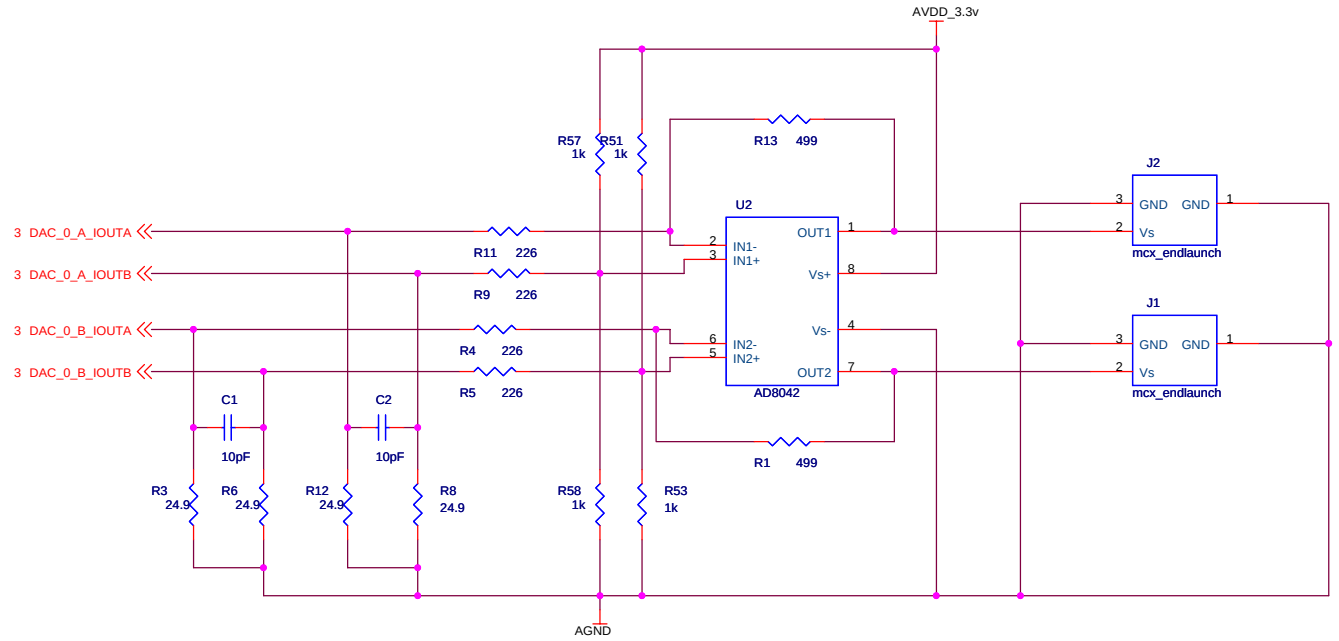

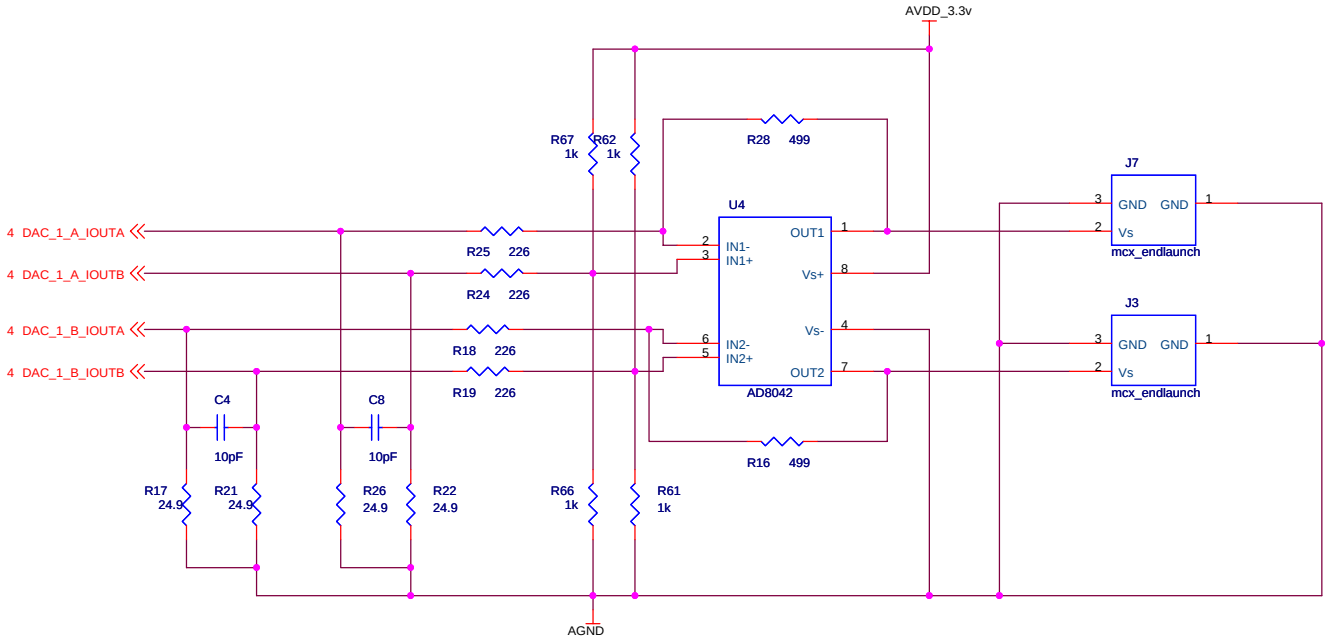

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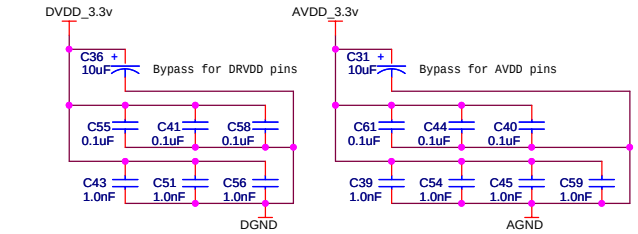

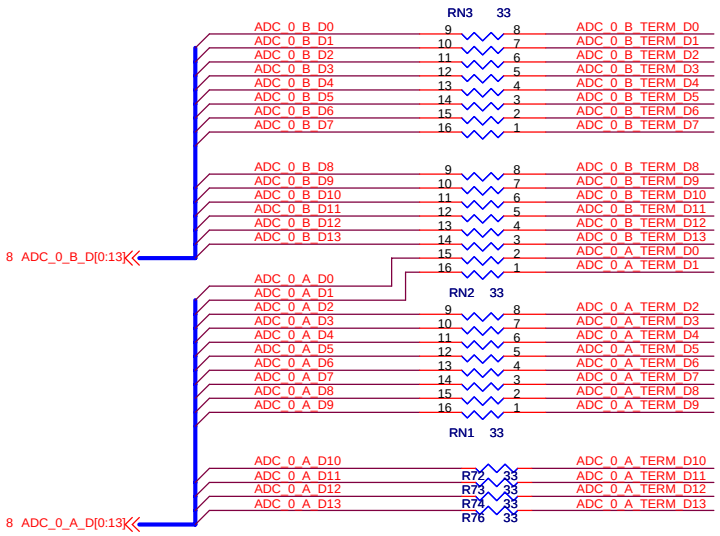

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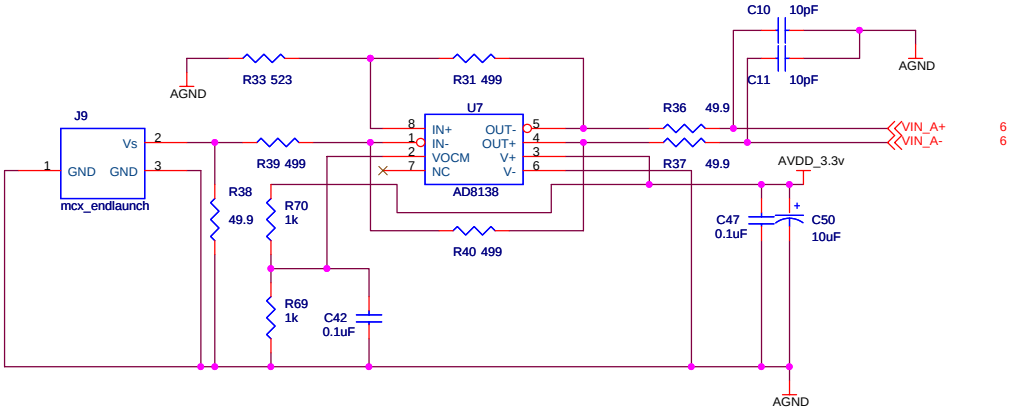

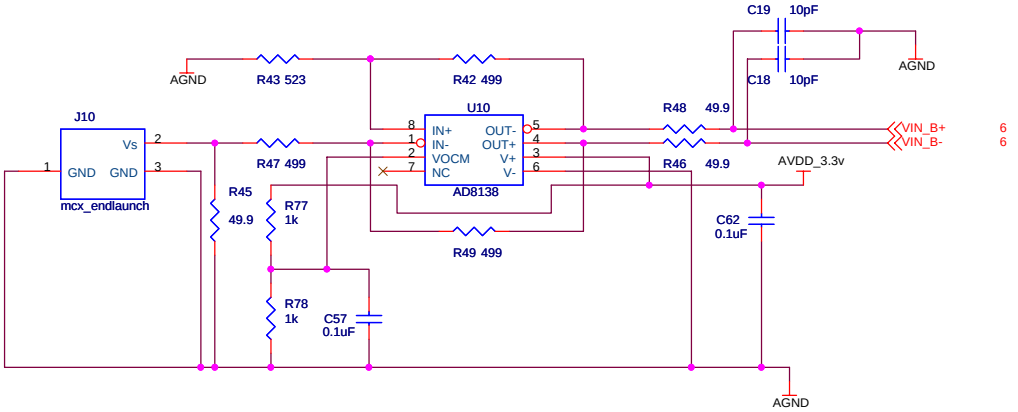

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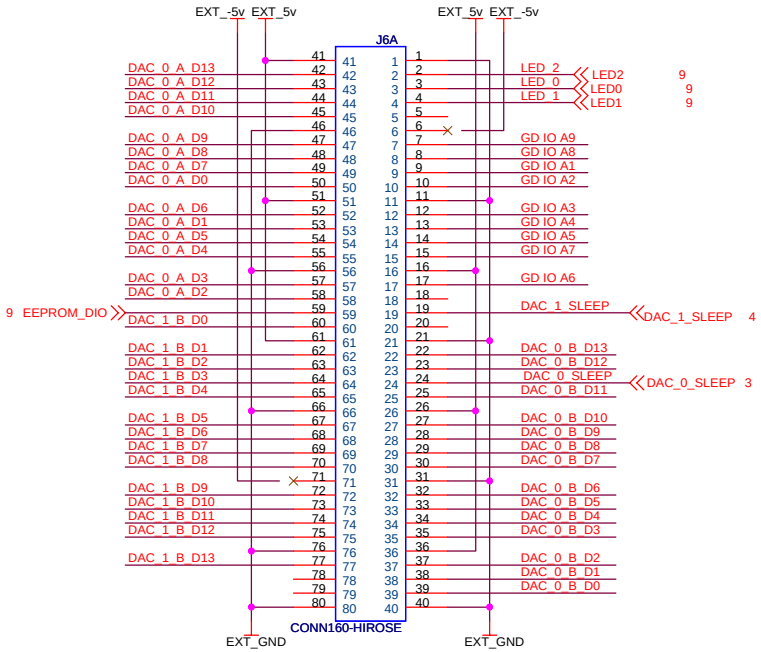

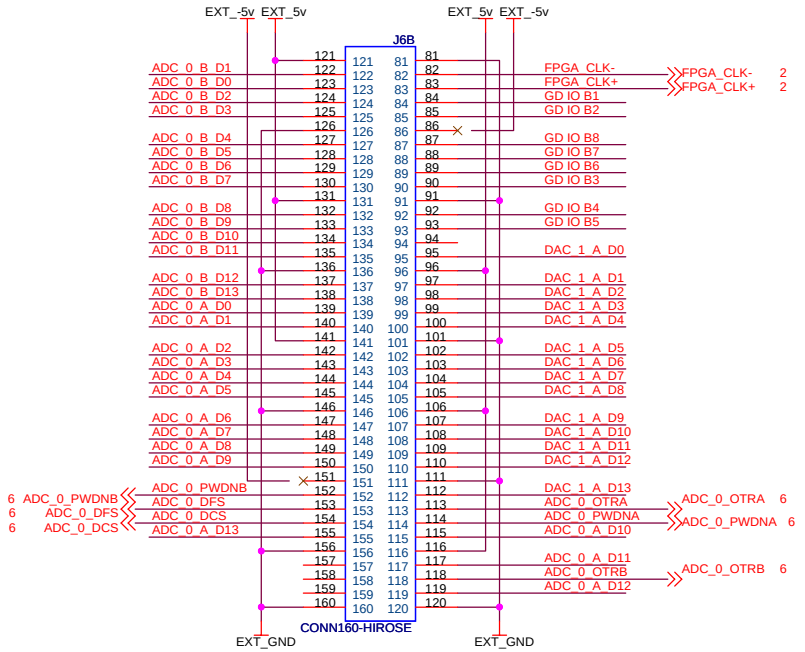

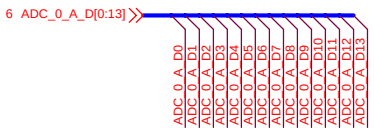

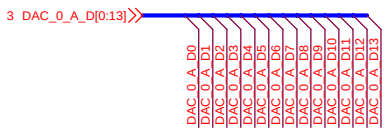

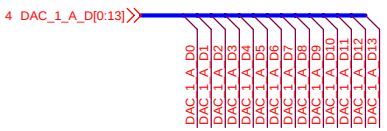

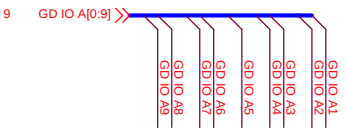

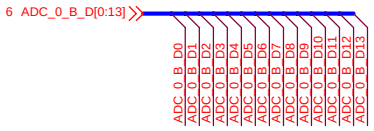

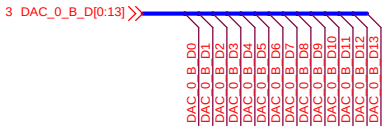

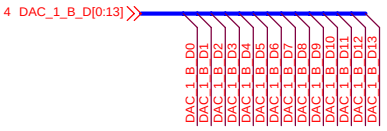

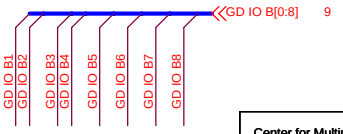

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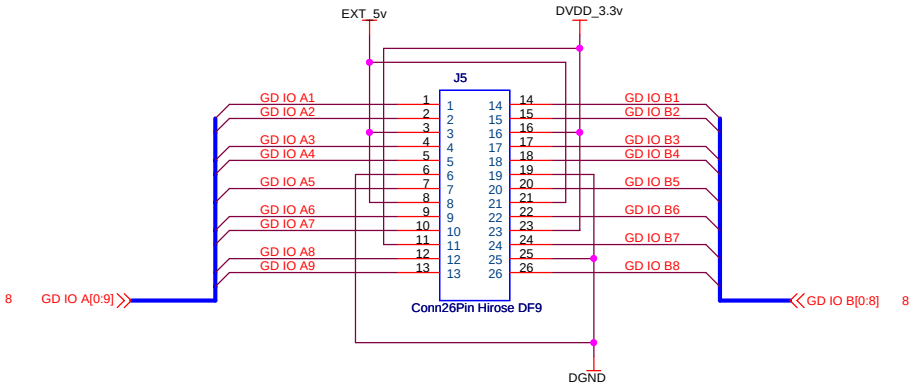

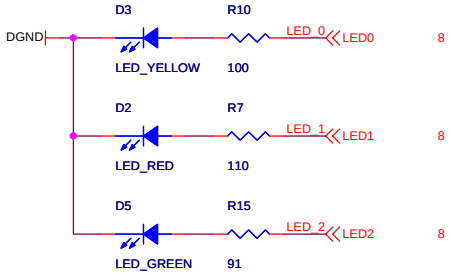

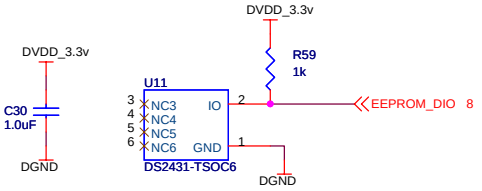

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