



Lab 1: Introduction to Xilinx Platform Studio & Base System Builder

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1 Introduction

In this lab, you will use Xilinx Platform Studio and Base System Builder to construct a simple hardware & software platform, then test your design on the WARP FPGA Board. The instructions for this exercise are on the WARP web site.

Please visit <http://warp.rice.edu/trac/wiki/Exercises/XPSIntro> to complete the exercise.